

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel;

wherein said switching TFT is controlled by a first gate signal output from said first gate signal line driver circuit;

wherein said erasure TFT is controlled by a second gate signal output from said second gate signal line driver circuit;

wherein said first EL driver TFT and said second EL driver TFT are controlled by said switching TFT or said erasure TFT; and

wherein said EL element is controlled by said first EL driver TFT and said second EL driver TFT, and

wherein said first EL driver TFT and said second EL driver TFT are electrically connected with said switching TFT.

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11. A light emitting device according to of claim 10, wherein said first EL driver TFT and said second EL driver TFT have the same polarity.

12. A light emitting device according to claim 10, wherein said switching TFT, said erasure TFT, said first EL driver TFT, or said second EL driver TFT is a top gate TFT.

13. A light emitting device according to claim 10, wherein said switching TFT, said erasure TFT, said first EL driver TFT, or said second EL driver TFT is a bottom gate TFT.

14. A light emitting device according to claim 10, wherein said source signal line driver circuit comprises a shift register, a first latch, and a second latch.

15. A light emitting device according to claim 14, wherein said first latch or said second latch comprises two clocked inverters and two inverters.

16. A light emitting device according to claim 10, wherein said first EL driver TFT or said second EL driver TFT is driven in a linear region.